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研究種目：基盤研究(B) (一般)

研究期間：2016～2018

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研究課題名(和文)半導体デバイスのミュオン誘起ソフトエラー率評価のための技術基盤構築

研究課題名(英文) Development of technical platform for estimation of muon-induced soft error rates in semiconductor devices

研究代表者

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交付決定額(研究期間全体)：(直接経費) 14,400,000円

研究成果の概要(和文)：国内ミュオン施設(J-PARC/MLF MUSEと阪大RCNP-MuSIC)において、65-nm SRAMデバイスに対するミュオン照射試験を実施した。実験結果は、負ミュオンの方が正ミュオンに比べてシングルイベントアップセット(SEU)断面積が大きいことを示した。シミュレーションにより、両者の相違は負ミュオン原子核捕獲反応に起因していることを明らかにした。さらに、宇宙線ミュオン計測装置を開発し、建屋内でエネルギー分布を取得し、モデル予測との整合性を確認した。照射試験、ミュオン計測、シミュレーション技術に対する研究開発を通じて、ミュオン起因ソフトエラー評価の技術基盤を構築できた。

研究成果の学術的意義や社会的意義

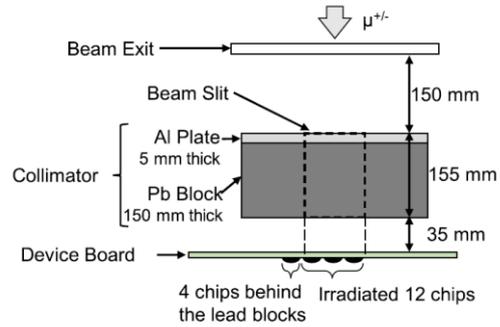
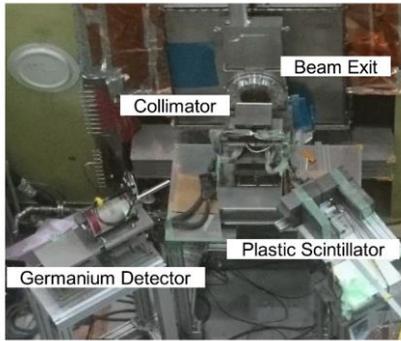
放射線による半導体デバイスの誤動作は確率的に稀にしか起こらない現象であるが、IoTの普及とともに世界中で膨大な数の半導体デバイスが使われているため、今後、発生数の増加が予想される。特に、社会インフラを支えている電子機器内でいったん誤動作がおこると、致命的な障害を起こす可能性がある。本研究では、次世代半導体デバイスで顕在化が危惧されている宇宙線ミュオンに起因するソフトエラーの発生機構を実験・シミュレーションにより解明し、エラー率推定のために基盤技術を開発した。その成果を次世代半導体デバイスの設計等に応用することで、車の自動運転やIoT分野の安心・安全な半導体技術創出への貢献が期待される。

研究成果の概要(英文)：We have conducted muon irradiation experiments for 65-nm SRAM devices at two domestic facilities (J-PARC/MLF MUSE and Osaka Univ. RCNP-MuSIC). The experimental results showed that the cross sections for negative-muon induced single-event upsets (SEUs) are much larger than those for positive-muon SEUs. It was clarified that this observation can be explained by the capture of stopping negative muons by nuclei in the device by means of muon transport simulation. Moreover, we have developed a portable spectrometer for cosmic-ray muons, and measured the muon energy distribution in the concrete building and confirmed the consistency between the measured data and model prediction. Thus, we have successfully established a technical platform for estimation of muon-induced soft error rates in semiconductor devices through the present technical development on irradiation experiments, cosmic-ray muon measurements, and soft error simulations.

研究分野：放射線理工学

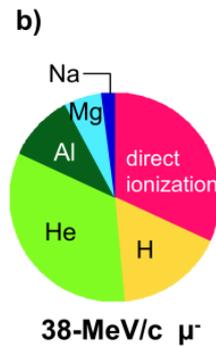
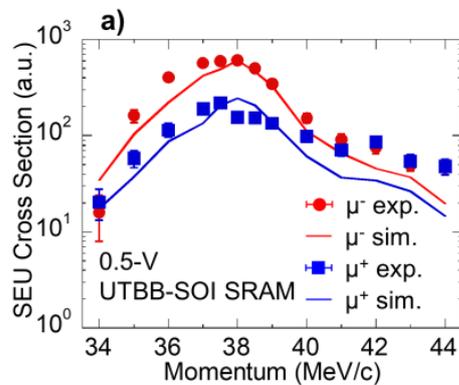
キーワード：ミュオン 半導体デバイス 照射試験 シングルイベントアップセット 宇宙線ミュオン計測 粒子輸送シミュレーション PHITS

(1) J-PARC MUSE 0.5V UTBB-SOI SRAM PKZ 4# p(1)w>
 65nm 0011f UTBB-SOI SRAM 4# p(1)w>
 PKZ J-PARC MUSE D2 2
 U00KS f 9%W0° 3v b 1e/4 & 7§1e > q
 r 031Z8• W24bW&gM1N
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 b1A3r63KS00KS
 IE)8§b U 7VMG SEU 8K 03A+ i
 5k710KS QU4.



W □ J-PARC MLF MUSE 0.5V 94Å

rN UTBB-SOI SRAM PKZ87Å 0.5V E • SEU 8UÑ
 5b60KZW0 (a)&gM 1E 38MeV/c EAE [KG
 b 4 5bvWS11b800M, SRAM Y
 b w8e (SV)EAE r[b10pMKZ> 5% 4Å SV 11A
 QY SEU 00C+ N IE110•
 \ 1E1E SEU 8b0E SEU 8(0E,
 AC M%0 11E 34#A• 42MeV/c 3b8kg
 0pK80
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W0 65-nm UTBB-SOI SRAM PM• SEU 8b)Y

S)Y W1&gM SRAM PK 38MeV/c 2E[8710K
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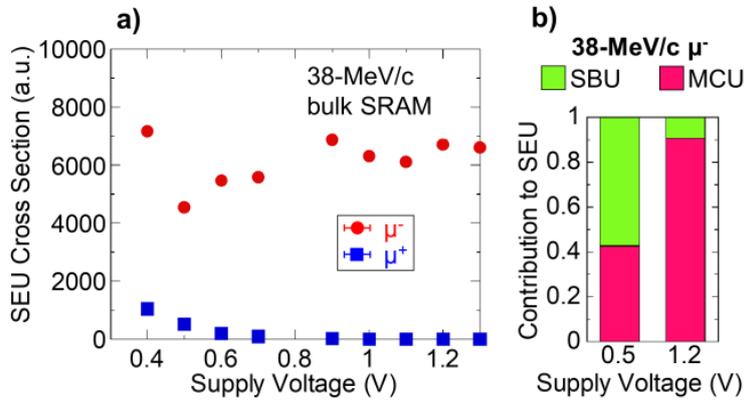
SEU (μ^-)

MCU bmcW

(b) &gM:~

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1\$ KZ MCU 8b6ZK



W 65-nm SRAM PM SEU 8b Y (MCU)

(a) lg5 SBU (b)

(2) RCNP-MuSIC E 100,

MuSIC 0Z

V3b

J-PARC MUSE 9[

MS

65nm

SRAM (EU00KSSc).

DC A

SEU 8b lgf..

op M+ M00AS

EXbS9m2&g[ASrS

MuSIC 0b

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SEU 8b

U5[c

SRAM 8KZ0SG

V 90EAS

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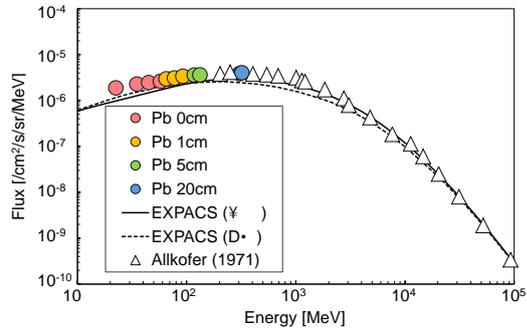
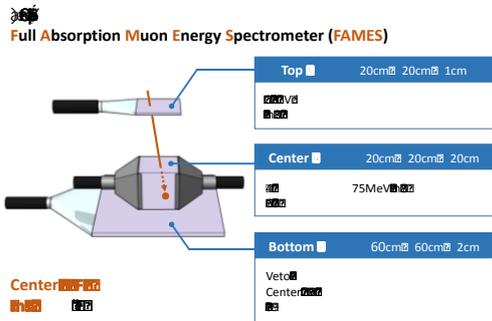
200MeV V

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100> 400MeV 8b f

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[2] T. Sato, PLoS ONE, vol. 10, no. 12, e0144679 (2015).

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